

# PCN\_SBC-SAMA5D36\_v001en

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Rev	Date/Signature	Changes
1	30.10.15/Bue	First Revision

## 1 Product affected

Product affected	Rev.	Replacement	Rev.
SBC-SAMA5D36	R2	SBC-SAMA5D36	R3

## 2 Change 1 – New board revision

### 2.1 Key Characteristics of the Change

The boards SBC-SAMA5D36-2 will no longer be manufactured by emtrion. They will be replaced by the new revision SBC-SAMA5D36-3 boards.

### 2.2 Description of Changes to the Customer

In general the characteristics of the R2 boards are kept unchanged. The following list contains the differences of the R3 boards:

- Reading the level of GPIO PC20 signals the board revision. PC20 is 0 at R3. It was a 1 before.
- Current consumption from the backup battery is reduced to 3 µA at R3 from 18 µA at prior revisions.
- The PMIC chip is connected to I<sup>2</sup>C interface TWI0 at R3. It was connected to TWI1 at R2.
- At R3 boards the voltage of a battery connected to J2 can be read by analog channel ADC5. The measured voltage conforms to  $V = (ADC\_CDR5 * 3300) / 2048$  mV
- The JTAG connector J13 is shrunk from 20 pin with 2.54 mm pitch to 10 pin with 1.27 mm pitch. Details about the connector can be found in the hardware manual.
- The chip select line of the NOR flash is interrupted by the series resistor R102 which is normally not populated. To use the NOR flash a solder bridge or a resistor with low value must be added.

### 2.3 Customer Impact of Change and Recommended Action

Normally no actions by the customer are needed.