

# GPIO Translation Table for DIMM-Series CPU Modules



History:

V1 06.03.2012/Sr:

V2 20.03.2014/Ko:

First Version

MX6, AM335x, Cadun, Tarion hinzugefügt

Tarion J12 Pin#	Cadun J12 Pin#	Verno J12 Pin#	Lothron J25 Pin#	Signal Name SODIMM Interface	DIMM-MX257				DIMM-MX53x				DIMM-MX6*1				DIMM-AM335x				DIMM-SH7723			DIMM-SH7724			DIMM-EMEV2		
					CPU Pin Nr	CPU Pin Name	Pin MUX Function Instance/Port	Direction	CPU Pin Nr	CPU Pin Name	Pin MUX Function Instance/Port	Direction	CPU Pin Nr	CPU Pin Name	Pin MUX Function	Direction	CPU Pin Nr	CPU Pin Name	Pin MUX Function	Direction	CPU Pin	Signal Name CPU	Direction	CPU Pin	Signal Name CPU	Direction	CPU Pin	Signal Name CPU	Direction
4	4	4	11	GPIO_0	W2	PWM	GPIO1/GPIO[26]	In/Out	N2	PATA_DATA8	GPIO2/GPIO[8]	In/Out	A18	NANDF_D0	GPIO2_I000	In/Out	C18	ECAPO_IN_PWM0_OUT	GPIO0_7	In/Out	PTZ4	GPIO_A	In/Out	PTA0	GPIO_A	In/Out	-	reserved	-
6	6	6	12	GPIO_1	F19	CSI_D4	GPIO1/GPIO[29]	In/Out	N3	PATA_DATA9	GPIO2/GPIO[9]	In/Out	C17	NANDF_D1	GPIO2_I001	In/Out	-	-	-	-	PTZ5	GPIO_B	In/Out	PTA1	GPIO_B	In/Out	PMIC	GPIO5	In/Out
-	8	8	13	GPIO_2	R3	UART2_RTS	GPIO4/GPIO[28]	In/Out	N4	PATA_DATA10	GPIO2/GPIO[10]	In/Out	F16	NANDF_D2	GPIO2_I002	In/Out	V15	GPMC_A5	GPIO1_21	In/Out	PTZ6	GPIO_C	In/Out	PTA2	GPIO_C	In/Out	GPIO_029	GPIO_029	In/Out
-	10	10	14	GPIO_3	G18	CSI_D5	GPIO1/GPIO[30]	In/Out	M6	PATA_DATA11	GPIO2/GPIO[11]	In/Out	D17	NANDF_D3	GPIO2_I003	In/Out	-	-	-	-	PTZ7	GPIO_D	In/Out	PTA3	GPIO_D	In/Out	GPIO_015	GPIO_015	In/Out
-	12	12	15	GPIO_4	-	-	-	-	N5	PATA_DATA12	GPIO2/GPIO[12]	In/Out	T2	SD4_DAT0	GPIO2_I008	In/Out	L16	MII1_RXD2	GPIO2_19	In/Out	PTG0	TPU0	Out	PTB6	TPU0	In/Out	GPIO_120	GPIO_120	In/Out
-	14	14	16	GPIO_5	G19	CSI_D8	GPIO1/GPIO[7]	In/Out	N6	PATA_DATA13	GPIO2/GPIO[13]	In/Out	B19	SD4_DAT1	GPIO2_I009	In/Out	K18	MII1_TX_CLK	GPIO3_9	In/Out	PTG1	TPU1	Out	PTB7	TPU1	In/Out	GPIO_121	GPIO_121	In/Out
-	16	16	17	GPIO_6	-	-	-	-	P6	PATA_DATA14	GPIO2/GPIO[14]	In/Out	F17	SD4_DAT2	GPIO2_I010	In/Out	L17	MII1_RXD3	GPIO2_18	In/Out	PTG2	TPU2	Out	PTR0	TPU2	In/Out	GPIO_031	GPIO_031	In/Out
18	18	18	18	GPIO_7	F20	CSI_D9	GPIO4/GPIO[21]	In/Out	P5	PATA_DATA15	GPIO2/GPIO[15]	In/Out	K22	EIM_LBA	GPIO2_I027	Out	L18	MII1_RX_CLK	GPIO3_10	In/Out	PTG3	TPU3	Out	PTR1	TPU3	In/Out	GPIO_030	GPIO_030	In/Out
20	20	20	19	GPIO_8	-	-	CAN2_RX	-	-	-	CAN2_RX	-	V5	KEY_ROW4	GPIO4_I015	In/Out	D17	UART1_RTsn	GPIO0_13	In/Out	PTX4	IRDA_IN#	In/Out	PTX6	IRDA_IN#	In/Out	GPIO_104	GPIO_104	In/Out
22	22	22	20	GPIO_9	-	-	CAN2_TX	-	-	-	CAN2_TX	-	T6	KEY_COL4	GPIO4_I014	In/Out	D18	UART1_CTSn	GPIO0_12	In/Out	PTX5	IRDA_OUT	In/Out	PTX7	IRDA_OUT	In/Out	GPIO_102	GPIO_102	In/Out

\*1 ab R2